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THE IMPACT OF INNER-PARAMETERS B-MAC PROTOCOL BY TAGUCHI METHOD FOR WSN

¹ALAA KAMAL YOUSIF, ²M.N.MOHD WARIP, ³ MOHAMED ELSHAIKH

¹University of Ha'il, College of Computer Science and Engineering

^{2,3} Embedded Network and advanced computing Research Cluster, School of computer and Communication engineering, University Malaysia Perlis, Level 1, Pauh Putra Main Campus, 02600, Arau, Malaysia E-mail: ¹loly.kam21@gmail.com, ²nazriwarip@unimap.edu.my, ³elshaikh@unimap.edu.my

ABSTRACT

The MAC protocols play an important role in the performance of wireless sensor network (WSN). MAC protocols are controlled with set of parameters from being dragged to undesired situation such as reduce the power consumption, listening idle, and overhead. This inner- parameters have direct impact on the efficiency of a MAC protocols and overall network performances. The impacts of theses parameters on reduce the power consumption are less considered. In the literature, a lot of studies concentrates on introducing a new protocols to reduce the power consumption for WSN. This paper aims to analysis the inner- parameters of MAC protocols for WSN power consumption by using Taguchi Delta Analysis (TDA). Moreover, the measure of inner - parameters is very important to find the optimal values to reduce the power consumption. This paper utilized Taguchi method to analysis the impact of B-MAC protocol parameters in WSN scenarios by exploits Taguchi delta analysis. Further, four inner - parameters are investigated in a simulation platform. Moreover, simulation experiments are carried out by OMNET++5 to prove the work in this paper. The obtained results show that inner- parameters B-MAC inner- protocol reduce the power consumption of WSN for two different scenarios.

Keywords: B-MAC, Taguchi Delta analysis (TDA), Power consumption, Taguchi method, WSN

1. INTRODUCTION

Wireless sensor network (WSN) attracts the attention in last years. Moreover, WSN exploits in many applications such as industrial sensing, environmental monitoring, home automation, process monitoring, healthcare applications, intelligent transportation systems, and military surveillance. WSN network contains sensor nodes collecting the information from the environment to perform a task. The sensing information forwarded to sink node. WSN deployed large amounts of sensor nodes in the harsh environment driven by a battery. However, the critical issue faced the WSN is limited battery capacity. Further, the sensor nodes need to work for a maximum time without depletion battery. MAC protocols for wireless sensor networks is challenging because the sensor nodes depend on limited capacity. Additionally, the lifetime of sensor nodes effects of entire lifetime network and the performance. In the literature, many MAC protocols are proposed based on prior energy efficient mechanisms[1][2] [3][4][5].

The work in this paper is aimed effects of the B-MAC inner- parameters for WSN by Taguchi method. Moreover, defined which one of the parameters have large effects of reducing the power consumption by delta analysis and find the optimum values for a set of defined B-MAC protocol parameters. Further, the framework for this paper is carried out through OMNET++5 and INET3.5 simulation experiments. The obtained set of optimal values is compared against the original parameters set defined in the B-MAC protocol.

The rest of the paper is organized as follow: Section two highlights some previous work in the same direction of this paper. Section three gives an overview of the Taguchi method and description of the proposed work. Section four reviews and discusses the obtained results of this work. Finally, the work in this paper is concluded in the last section. ISSN: 1992-8645

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efficiency in a wireless sensor network. Scheduled Channel Polling (SCP) reduces the preamble by combining preamble sampling and scheduling techniques; this process provides more efficiency for power con and low duty cycles. Scheduled channel polling (SCP) inherits significant characteristics from SCP-MAC and takes into consideration minimization of delay and contention in the context of remote monitoring and data gathering applications. Furthermore, it strives to extend the network lifetime and curtails the delay limitations [8].

Min et al. (2015) presented an Energy and Latency-Aware MAC (ELA-MAC) protocol that provides high energy-efficiency and reduces the delay for data collection applications, in wireless sensor networks. ELA-MAC protocol spreads collection-request without using an early ACK and delivers collection response using an early ACK, with due consideration on the traffic types. In this way, power consumption on both downlink and uplink can be minimized. Besides, the time for collecting data can be shortened on the up- link as well [9].

Morshed et al. (2016) introduced the Traffic-adaptive duty cycle adaptation. TR-MAC is a WSN protocol for energy efficiency by using the preamble sampling protocol, to provide maximum sleeping time for sensor nodes and active periodically separate from other nodes to sample the shared medium for any activity. The protocol works in two states: firstly, unsynchronized sender; begins to send preamble with the small data packet and waits for ACK from the destination; the source iterates the process until it receives the ACK. After receiving the preamble-data packet, the destination node forwards an ACK back to the source. Secondly, synchronized link states; after this unsynchronized communication link state, each node could remember the others next active time and move to the synchronized link state. If the source remembers the destination's next active state, called receiver-driven synchronization, the source holds its packet transmission till the destination's next active time. Thus, the source node successfully ignores much extra iteration of the data and acknowledges listen cycles to save energy. The simulation result shows that there is an improvement in terms of an end-to-end delay and power consumption for stable traffic [10].

Chen et al. (2017) introduced Light-Weight Opportunistic Forwarding (LWOF) protocol for energy efficiency in WSN. LWOF protocol tune the preamble length for specific

2. RELATED WORK

Energy consumption is considered as major design challenge for WSN. The energyefficient MAC protocols for WSN had been reviewed and introduced in many previous works by introducing a new protocol.

Corbellini et al. (2012) presented a lowlatency asynchronous access method (LA-MAC) for efficient forwarding, in wireless sensor networks. Moreover, the uniqueness of the design of LA-MAC is that there is no existing MAC access method that can be compared to its characteristic behavior (throughput, power efficient, and an endto-end delay) for different network parameters. LA-MAC works similar to X-MAC, in that when the source needs to send data packet active, it checks the shared medium, and forwards preambles. However, after the early acknowledgment is received, the source returns to sleep again and waits for the SCHEDULE message to be forwarded. When the destination receives one preamble, it clears it and completes check on the channel (polling period) to detect additional possible preambles to clear. After finished checking the channel, the destination processes requests and broadcasts the SCHEDULE message. The result of simulation shows that LA-MAC provides the best performance in the case of high bitrate networks under traffic congestion [6].

Han et al. (2013) proposed a power efficient asynchronous MAC protocol (WX-MAC) for the wireless sensor network. WX-MAC shortens the preamble and ensures the sensor nodes remains in an active mode as short as possible. WX-MAC minimizes the power consumed by the sources and receivers as well as reduces the chances of overheating on the surrounding sensor nodes, which provides more efficiency on the power preservation. WX-MAC shortens the preamble by keeping sensor nodes awake for a short period as possible. Further, WX-MAC enables sensor nodes to interchange the sampling schedules. Then, the sensor node uses the sampling schedules to define the beginning of its preamble based on estimating its target's sampling schedules. Also, the strobe preamble approach stops the preamble, begins the transmission process, and backs to sleep mode, at appropriate times. The simulations result indicates that WX-MAC outperforms, regarding power consumption, compared with B-MAC, Wise- MAC, and X-MAC [7].

Zhang et al. (2013) introduced scheduled channel polling-MAC (SCP) protocol for energy

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are the responsibility of the designer. Further, the control factor can determine more than single value called level. This process is performed to find the best level of these factor for objective design.

Noise factors indicate uncontrollable factors. The uncontrollable factors affect the response and their levels change on the unit of the product, environment, and time.



Figure 1: Taguchi Process Diagram (P-Diagram).

Recently, Taguchi method is utilized in various technique to improve the quality [16] [17]. Taguchi method works through four phases: planning phase, experimental phase, Delta analysis phase, and validation experiment as illustrated in Figure 2. The following subsection explains the implementation of Taguchi methods in the work of this paper.

forwarding probability to node density and sleep duration. LPL-MAC used two scheme firstly, lightweight opportunistic forwarding scheme (LWOF) to provide reliable and timely data delivery for WSN with asynchronous duty cycles. Secondly, preamble length of LPL MAC protocol is optimized by using the non-deterministic characteristic of opportunistic forwarding, according to forwarding probability, node density, and sleep duration [11].

Alahmadi & Bouabdallah (2019) a multichannel preamble sampling MAC protocol, (MCPS) for WSN. MCPS is a low-power MAC protocol operating on multichannel using carrier sensing for collision avoidance. Specifically, MCPS uses all the non-overlapping channels provided by IEEE 802.15.4 physical layer. MCPS uses one dedicated common control channel to wake up an intended receiver using a preamble sampling technique [12].

3. TAGUCHI METHOD(TM)

Genichi Taguchi is a Japanese researcher, who developed an optimization concept that depends on quality management, dated back to the late 1940s. The concept embodied a set of statistical methods and equations to find the optimum point. Taguchi method is one of the branches of the design of experiments (DOE) methods, introduced in many works in different scientific applications. Generally, DOE methods share a common objective to determine the relationship between the control factors and the response. TM utilizes orthogonal arrays and statistical equations to evaluate measuring system. The most important part of the Taguchi technique is quality loss function. Taguchi has recognized signal to noise ratio (S/N) as performance statistics. Figure 1 explains the block diagram of a product as specified by TM. Also it could be utilized to explain a manufacturing process or even a business system. The output is represented by y. The parameters which affect the output are classified into three as follows [13] [14] [15]:

- a) Signal factors refer to the input factors, which are determined by the user to gain more performance or to accurate the specific output. Further, the input factors chosen by the engineer depended on the engineering knowledge.
- b) Control factors refer to system values to signal factors. The control factor values



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Figure 2. Taguchi method phases.

3.1 Planning phase

The first phase in TM aims to define the parameters that need optimization process. Further, the determining process for the parameters depend on whose have effectiveness to provide better quality, the output or response of these parameters must be observed. Four B-MAC inner- parameters selected for this work as explained in Table 1. Further, the parameters exploit to reduce the power consumption for two WSN scenarios (Farm, Part of farm).

Table 1. Inner- parameters B-MAC protocol (ZigBee standard).

Control factors	level 1	level2	level 3
Slot duration (ms)	1	2	4
Bit rate (kbps)	20	40	250
Check interval (µs)	128	800	1600
Max power transmission (mW)	1	5	10

Subsequently, select the convenient orthogonal array for the control factors and the number of levels. The suitable orthogonal array is selected to design the experiments table and the data analysis procedure. Orthogonal array is standard for experimental design that performs the optimization process with only a small number of experimental trials

The choice orthogonal array is L9 (3^4) . Moreover, the objective of this process is finding the optimal values for inner-parameters and large impact of inner-parameters by Delta analysis.

experiment	Slot	Bit	Check	Tx
No	duration	rate	interval	power
1	level1	level 1	level 1	level 1
2	level 1	level 2	level 2	level 2
3	level 1	level 3	level 3	level 3
4	level 2	level 1	level 2	level 3
5	level 2	level 2	level 3	level 1
6	level 2	level 3	level 1	level 2
7	level 3	level 1	level 3	level 2
8	level 3	level 2	level 1	level 3
9	level 3	level 3	level 2	level 1

Table 2. L9 (3^4) orthogonal array.

3.2 Experimental Phase

The second phase in TM, according to the orthogonal array is achieved by conducting the experiments table and recording the results. Experiments design in TM is expected to produce a measure of the output targets (Power consumption) which are to be used in the analysis phase. Outputs are presented according to trails for further calculate the objective functions. Experimental layout with the selected value of parameters for nine experiments carried out is illustrated in Table 3.

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B- Nominal the best refers to reducing variability around a target:

$S/N_{\text{Norminal-the-best}} = -10 \times \log \sum_{i=1}^{n} \frac{Y^{i}}{S^{2}}$ (2)

C- Larger the better refers to the system response will be as large as possible:

$$S/N_{larger-the-better} = -10 \times \log \sum_{i=1}^{n} \frac{1/Y^2}{n}$$
(3)

Where

- i= Experiment number
- n = Number of trials for experiment i

y= the mean of experiment i

s =Standard deviation of experiment i

Outputs are presented according to Measure values illustrated in Table. To accomplish this goal exploited OMNET++5 simulator and INET3.5 framework. The experimental results is illustrated in Table 4 and Table 5.

The simulation experiments are conducted on OMNET++5 simulator and INET3.5 framework. Moreover, the farm and part of farm scenarios are designed based on the INET3.5 framework. The part of farm scenario contains 43 sensor nodes and low bit rate traffic. On the other hand, farm scenario contains 16 nodes with high bit rate traffic. Further, the experiments will be repeated with different random seed for two scenarios. The obtained results of orthogonal array experiments exploits in the analysis phase.

3.3 Analysis Phase

The third phase comes after the experiments are carried out. TM results analysis uses the signal- to –noise equations to measure the performance (Equation 1, 2, and 3). Then, TM output for power consumption illustrated in Table 3.4 achieves delta analysis. The measures the impact of the control factors on the output, and this result is obtained by the delta analysis of loss function. [18] [19].

A- Smaller the better refers to the system response will be as small as possible:

$$S/N_{smaller-the-better} = -10 \times \log \sum_{i=1}^{n} \frac{Y^2}{n}$$
(1)

Experiment	Slot	Bit	Check	Tx
No	duration	rate	interval	power
				1
1	1	20	128	1
2	1	40	800	5
3	1	250	1600	10
4	2	20	800	10
5	2	40	1600	1
6	2	250	128	5
7	4	20	1600	5
8	4	40	128	10
9	4	250	800	1



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Table 4. Power consumption result from OMNET++ by OA experiments for part of farm scenario.

Experime nt NO	R1*	R2*	R3*	R4*
1	0.46356	0.47572	0.48494	0.49418
	2	0	8	0
2	0.40331	0.40198	0.40171	0.40095
	9	7	9	6
3	0.35572 8	0.35587 0	0.35506	0.35513 3
4	0.35170	0.37014	0.60317	0.35522
	8	1	0	3
5	0.31079	0.30946	0.30952	0.30864
	6	6	0	0
6	0.31771	0.31947	0.32253	0.32500
	2	7	2	9
7	0.31329 1	0.31292 7	0.31238 6	0.33991 4
8	0.22753	0.22728	0.22720	0.22809
	3	7	6	8
9	0.21605 4	0.21769 7	0.22181 5	0.22382 5

Table 5.	<i>Power consumption result from OMNET++ by</i>
	TOA experiments for farm scenario.

Exper iment NO	R1*	R2*	R3*	R4*
1	0.14300 2	0.146192	0.1492 72	0.1519 43
2	0.12597 4	0.125229	0.1253 43	0.2830 92
3	0.11065 1	0.110662	0.1105 46	0.1101 92
4	0.10696 0	0.112299	0.1091 91	0.1084 89
5	0.09679 2	0.096675	0.0966 25	0.0962 21
6	0.09667 6	0.097361	0.0982 74	0.0989 31
7	0.09805 2	0.097740	0.0977 03	0.1061 13
8	0.06841 6	0.068695	0.0685 73	0.0686 86
9	0.06481 9	0.065419	0.0662 43	0.0666 91

The objective of this research optimizes the power consumption for B-MAC protocol by using TM. TM has three equations each one of them is designated for a specific category of targets, to minimize the power consumption used smaller the better Equation 1. S/N ratios for the power consumption of all the experiments were calculated and tabulated as shown in Table 6 and Table 7.

Table 6.	Tabulated S/N ratios power
consumption	for part of farm scenario.

Experiment NO	S/N
1	6.380385674
2	7.915562028
3	8.984461953
4	7.265933986
5	10.18379974
6	9.864631872
7	9.901232005
8	12.85917828
9	13.15669008

Table 7.	Tabulated S/N ratios pow	er
consumpti	on for farm scenario.	

Experiment NO	S/N
1	16.61590932
2	14.96877953
3	19.13173936
4	19.23140784
5	20.30239185
6	20.19195456
7	20.00291648
8	23.27445564
9	23.63588015

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After tabulated the S/N ratio for each experiment, calculate the average S/N value for each factor and level. The results of this process for all parameters illustrated in Table 8 and Table 9.

Table 8.S/N for part of farm power consumption.

experi ment NoSlot duratio nBit rateCheck interva 1Tx powerS/N1level1 level1level 1 llevel 1 level 1level 1 level 16.380385 6742level1 level 1level 1 level 2level 1 level 2level 1 level 2f.380385 6743level 1 level 1level 2 level 2level 2 level 3level 2 postf.915562 0283level 1 level 1level 3 level 3level 3 posts.984461 post4level 2 level 2level 3 level 3level 3 posts.984461 post5level 2 level 2level 3 level 3level 3 posts.984461 post6level 2 level 2level 3 level 3level 1 level 3lo.18379 post7level 2 level 3level 1 level 3level 2 level 3g.901232 post7level 3 level 3level 1 level 3level 3 level 3level 3 s289level 3 level 3level 2 level 2level 1 level 3level 3 level 3			1	1	1	r
$\begin{array}{c c c c c c c c c c c c c c c c c c c $	experi	Slot	Bit	Check	Tx	
No n 1 1 level1 level 1 level 1 level 1 6.380385 2 level 1 level 1 level 1 level 1 level 1 6.380385 2 level 1 level 2 level 2 level 2 2028 3 level 1 level 2 level 2 level 3 8.984461 4 level 2 level 3 level 3 level 3 986 5 level 2 level 1 level 3 level 3 986 6 level 2 level 3 level 3 level 1 10.18379 7 level 2 level 3 level 3 level 2 9.901232 7 level 3 level 3 level 3 level 3 9.901232 8 level 3 level 1 level 3 level 3 828 9 level 3 level 3 level 3 level 3 828 9 level 3 level 2 level 1 level 1 008 <td>ment</td> <td>duratio</td> <td>rate</td> <td>interva</td> <td>power</td> <td>S/N</td>	ment	duratio	rate	interva	power	S/N
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	No	n		1	-	
$\begin{array}{c c c c c c c c c c c c c c c c c c c $	1					6.380385
$\begin{array}{c c c c c c c c c c c c c c c c c c c $		level1	level 1	level 1	level 1	674
$\begin{array}{c c c c c c c c c c c c c c c c c c c $	2					7.915562
$\begin{array}{c c c c c c c c c c c c c c c c c c c $		level 1	level 2	level 2	level 2	028
$\begin{array}{c c c c c c c c c c c c c c c c c c c $	3					8.984461
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$		level 1	level 3	level 3	level 3	953
$\begin{array}{c c c c c c c c c c c c c c c c c c c $	4					7.265933
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$		level 2	level 1	level 2	level 3	986
$\begin{array}{c c c c c c c c c c c c c c c c c c c $	5					10.18379
$\begin{array}{c c c c c c c c c c c c c c c c c c c $		level 2	level 2	level 3	level 1	974
$\begin{array}{c c c c c c c c c c c c c c c c c c c $	6					9.864631
7 level 3 level 1 level 3 level 2 9.901232 8 level 3 level 4 level 3 level 2 005 9 level 3 level 4 level 1 level 3 level 3 level 3 9 level 3 level 4 level 2 level 1 level 3 level 3 1 1 level 3 level 4 level 3 level 3 level 3 9 level 3 level 2 level 1 level 1 level 1		level 2	level 3	level 1	level 2	872
level 3 level 1 level 2 005 8 level 3 level 1 level 2 005 9 level 3 level 1 level 3 828 9 level 3 level 2 level 1 13.15669 008 3 level 2 level 1 008	7					9.901232
8 level 3 level 1 level 1 level 3 12.85917 9 12 12 12 12 12 12 9 12 12 12 12 12 12 9 12 12 12 12 12 13 15 9 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 <td></td> <td>level 3</td> <td>level 1</td> <td>level 3</td> <td>level 2</td> <td>005</td>		level 3	level 1	level 3	level 2	005
level 3level 1level 1level 38289level 3level 1level 2level 113.156699level 3level 2level 1008	8					12.85917
9 level 3 level 2 level 1 13.15669 3 008		level 3	level 2	level 1	level 3	828
level 3 level 1 level 2 level 1 008	9					13.15669
		level 3	level 3	level 2	level 1	008

experi	Slot	Bit	Check	Tx	
ment	duratio	rate	interva	power	S/N
No	n		1	1	
1					16.61590
	level1	level 1	level 1	level 1	932
2					14.96877
	level 1	level 2	level 2	level 2	953
3					19.13173
	level 1	level 3	level 3	level 3	936
4					19.23140
	level 2	level 1	level 2	level 3	784
5					20.30239
	level 2	level 2	level 3	level 1	185
6					20.19195
	level 2	level 3	level 1	level 2	456
7					20.00291
	level 3	level 1	level 3	level 2	648
8					23.27445
	level 3	level 2	level 1	level 3	564
9					23.63588
	level 3	level 3	level 2	level 1	015

Delta analysis is utilizing to measuring the impact of the inner parameters on the output. Further, the optimum values are obtained by comparing the averages S/N for all innerparameters levels. Moreover, Delta is the difference between S/N of factors (Delta = high S/N – Low S/N).

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 Table 10. Delta analysis of the S/N power consumption

 value for part of farm scenario.

level	Slot	Bit	Check	Tx
	duration	rate	interval	power
1	7.760	7.849	9.701	9.907
2	9.105	10.320	9.446	9.227
3	11.972	10.669	9.690	9.703
DL	4.010	0.010	0.055	0.600
Delta	4.212	2.819	0.255	0.680
Rank	1	2	4	3

Table 11.	Delta analysis	of the S/N	power	consumption
value for farm scenario.				

level	Slot	Bit rate	Check	Tx
	duration		interval	power
1	16.91	18.62	20.03	20.18
2	19.91	19.52	19.28	18.39
3	22.30	20.99	19.81	20.55
Delta	5.40	2.37	0.75	2.16
Rank	1	2	4	3

Table 10 and Table 11 explained the slot duration has the largest effects on the reduce power consumption, while the transmission power has the least for part of farm and farm scenarios.

3.4 Validation Experiments

The validation phase carried out the optimal values defined in analysis phase by OMNET++5 simulator. On the other hand, validation experiments are comparing between Taguchi B-MAC with the B-MAC result. Figure 3 and Figure 4 shows the comparison between Taguchi B-MAC protocol and B-MAC protocol for farm and part of farm. Finally, Taguchi B-MAC reducing the power consumption for farm scenario about 71.07% and for part of farm about 45.71%.



Figure 3. Comparison between TM- BMAC and B-MAC for part of farm Scenarios.



Figure 4. Comparison between TM- BMAC and B-MAC for farm Scenarios.

4. CONCLUSION

The objective of this paper is studying the impacts of inner parameters B-MAC protocol by Taguchi Delta Analysis (TDA). Further, two WSN scenarios are considered for the purposes of this work namely farm and part of farm. TDA explained inner- parameters B-MAC protocol can effect on reducing the power consumption. Further, the bit rate has the largest effects on the reduce power consumption for farm scenario. While the check interval has the largest effects on the reduce power consumption. Furthermore, Taguchi B-MAC it

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outperforms of B-MAC protocol in terms of power [9 consumption.

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