

# ABOUT THE FUNCTIONING OF THE SPECIAL-PURPOSE CALCULATING UNIT BASED ON THE LINEAR SYSTEM SOLUTION USING THE FIRST ORDER DELTA-TRANSFORMATIONS

<sup>1</sup> LIUBOV VLADIMIROVNA PIRSKAYA, <sup>2</sup> NAIL' SHAVKYATOVISH KHUSAINOV

<sup>1,2</sup> Institute of Computer Technology and Information Security, Southern Federal University,

Rostov-on-Don, Russia

E-mail: <sup>1</sup> lyubov.pirskaya@gmail.com, <sup>2</sup> khusainov@sfnedu.ru

## ABSTRACT

This paper discusses the theoretical representations of special-purpose calculating unit functioning for the iteration solution of linear systems using the first order delta-transformations and variable quantum. It is considered the algorithm for iterative solution of linear systems based on the first order delta-transformations and variable quantum is adapted for implementation in a special-purpose calculating unit. A special feature of special-purpose calculating unit functioning based on this algorithm is the implementation of the introduction at the beginning of each cycle  $l$  of a new variable quantum value that is reflected in the current cycle when the residual values and the unknown variable are formed by shifting them to the left by 1 or 2 bits. Formation of unknown variables in the unit is carried out by adding or subtracting the signs of quantum of the first and second variables differences, taking at each iteration the values  $\pm 1$ . This feature of variable normalization represents the possibility of organizing a computational process on the basis of an integer data representation. At the final step of the algorithm operation in the unit, it is possible to bring the values of the variables to the original real form, taking into account the weight of the minimum transformation quantum. With the orientation to FPGA, comparative estimates are obtained for the hardware and time resources of the developed algorithm and comprehensive comparative estimate of the effectiveness for special-purpose calculating unit functioning. In this paper for the developed algorithm of the unit functioning, it is shown that it is possible to reduce the execution of one iteration and the iterative process as a whole, the amount of hardware resources and generally improve the efficiency in comparison with the special-purpose calculating unit functioning based on the simple iteration method.

**Keywords:** *Special-Purpose Calculating Unit, Linear System Solution, First Order Delta-Transformation.*

## 1. INTRODUCTION

The issues study of qualitative and quantitative improvement of computing devices performance for solving complex practical problems in real time shows that using the most modern universal computer technology there are various difficulties: the need to organize effective parallel computing processes with the decision to reduce the amount of transferred information, the number of simultaneously operating multipliers, multi-bit registers for storing information, simplification of complex systems of information exchange, ensuring high performance of calculators in processing information. Such problems arise, in particular, in the organization in real time of a

parallel solution of practical problems, that can be reduced to problems of computational mathematics, to design of on-board computer systems, specialized control devices, and so on. One of the ways to solve these problems is the creation of specialized computing devices and systems, taking into account the design of their problem-oriented purpose and using special effective methods for implementing the computational process [1].

The necessity of problem-oriented computations arises, for example, when in real time solving problems of local navigation [2]-[4], in particular, determining the aircraft coordinates, which reduces to the problem of solving linear algebraic equations systems (linear systems). The

use of problem-oriented computations under the given conditions allows organizing the computing process in such a way that the processing of information at the level of one iteration is performed with sufficient accuracy, high speed and with an extremely long time step. Then, it is possible to demand the lowest requirements to the performance of computing tools, taking into account the possibility of simultaneous implementation of other algorithms and programs.

The functioning of a special-purpose calculating unit based on known iterative algorithms for solving linear systems [5]-[8] is characterized by a huge amount of hardware resources, which is associated with the need to implement a large number of multi-bit multipliers of coefficients and variables, as well as to transfer of multi-bit codes between the equations of the system at parallel implementation. The number of hardware and time resources increases, when it is necessary to simultaneously solve a large number of linear systems.

The developed algorithms for solving linear systems with constant and variable free terms based on the first order delta-transformations with constant [9]-[10] and variable quantum [11]-[16] when implemented in a special-purpose calculating unit allow organizing a computing process with the exception of operations of a multi-bit multiplication and obtaining a result in one iteration of the steady-state process. However, in papers [9]-[11] the use of a constant quantum is characterized by a much larger number of iterations. The use of an variable quantum greatly reduces the number of iterations [12]-[18].

The essence of these algorithms with a variable quantum lies in the representation of the iterative process in the form of  $l$  iteration cycles, in each of which a parallel for all equations of a linear system is performed the formation of variables at a constant modulus of transformation quantum [14]-[16].

In the paper [11] the process of linear systems solution must be preceded by the number of iterations in the cycle, using, in particular, the division operation that under the conditions of the perform these calculations with the help of the special-purpose calculating unit is not appropriate.

Almost all the papers [11]-[13] concerned the algorithmic organization of the iterative process

for linear systems solving with the use of the first order delta-transformations with the variable quantum has not got any information about the theoretical justification for the selection of the best relation between the quanta of the adjacent cycles; on the one hand the ways to specify these relations are introduced heuristically, on the other hand they are not fully cover the theoretical and practical interest of these relations.

In addition, the following observations should be noted. In the papers [11]-[13] the proposed algorithms are characterized by high computational complexity when implementing the iteration process due to the necessity of performing the squaring of the discrepancies across all equations at each iteration, summing them and highlight the smallest of the current according to the amount of values across the iterations. The squaring of the discrepancies is associated with the need to use the multibit multipliers, which is contrary to the original goal – implementation of the special-purpose control units of linear systems solution with the possible of the exception of such devices. The iterations termination moment is fixed either with the use of a certain constant, the determining value of which is uncertain, or according to the number of iterations that is also a problem of the preliminary estimate.

In the papers [14]-[16] is developed a theoretical substantiation of the selection of the number of cycles and the values of the variables quantum, aimed at minimizing the number of iterations in the linear systems solution on the basis of the first order delta-transformations. The algorithms which showed efficiency on ensuring timely completion of iterations, simple in realization and not demanding any numerical assessment with use of special basic data are introduced for the formation of the completion moment of the real iterative processes in the loops.

The architecture of the special-purpose calculating unit can undoubtedly impact on final algorithmic efficiency. And in the design of special-purpose calculating unit using the FPGA peculiarities of algorithmic software allow to achieve the highest quality performance indicators to minimize the costs of resources (equipment) compared with known [19]-[21].

Thus, on the basis of the algorithm presented in [14]-[16], the paper presents the algorithm adapted to the implementation in a

special-purpose calculating unit for the parallel iterative solution of linear systems based on the first order delta-transformations and variable quantum. Next, the paper contains the developed architecture, features and a study of the effectiveness of the special-purpose calculating unit functioning based on the linear system solution using the first order delta-transformations and variable quantum.

**2. THE ALGORITHM OF A LINEAR SYSTEM SOLUTION ADAPTED UNDER REALIZATION IN A SPECIAL-PURPOSE CALCULATING UNIT**

There is an linear system that has a matrix of constant coefficients and, in general case, variable free terms, fulfilling convergence conditions described in papers [5]-[8]:

$$BY^*(t) = G(t). \tag{1.1}$$

Let's transform the system

$$Y^*(t) = AY^*(t) + D(t),$$

and transfer to writing with residual  $z(t)$  for applying the iteration method:

$$z(t) = Y(t) - AY(t) - D(t). \tag{1.2}$$

In the given systems  $B = [b_{ij}]$ ,  $A = [b_{ij} / b_{rr}]$  are matrices of dimensional coefficients  $n \times n$ ;  $G(t)$ ,  $D(t)$  are column-vectors of absolute system terms (in particular for the system with fixed absolute terms  $G(t) = G = [g_r]$ ,  $D(t) = D = [g_r / b_{rr}]$ );  $Y^*(t)$  are column-vectors of system unknowns;  $z(t)$ ,  $Y(t)$  are column-vectors of residuals and approximate unknown values;  $t$  is an independent variable;  $\det A \neq 0$ .

Based on the results obtained in [14]-[16], an algorithm for the parallel solution of linear system (1.1) with constant free terms using the first order delta-transformations and variable quantum oriented for special-purpose calculating unit is presented below in the following difference form for  $i$ -step under the initial conditions  $Y_{r01} = 0$ ,  $z_{r01} = -D_r$ ,  $|z_{01}|_{\max}$ ,  $r = \overline{1, n}$ ,  $c_p^* = 2^{-s}$ ,  $s \in N$  [17]-[18]:

- calculating the values of residuals and unknowns before each iteration cycle:

$$z_{r0l} = z_{rR_{\text{int},(1,2)}^*(l-1)} \cdot R_{\text{int},(1,2)}; \tag{1.3.1}$$

$$Y_{r0l} = Y_{rR_{\text{int},(1,2)}^*(l-1)} \cdot R_{\text{int},(1,2)}; \tag{1.3.2}$$

$$r = \overline{1, n}, l = 1, 2, \dots, P_{\text{int},(1,2)};$$

- creation of first difference quantum signs at each iteration in cycles:

$$\Delta_{ril} = -\text{sign}(z_{r(i-1)l}); \tag{1.3.3}$$

$$\Delta_{ril} \in \{+1, -1\}; r = \overline{1, n}, i = 1, 2, \dots, R_{\text{int},(1,2)}^*;$$

$$l = 1, 2, \dots, P_{\text{int},(1,2)};$$

- demodulation:

$$Y_{ril} = Y_{r(i-1)l} + \Delta_{ril}; \tag{1.3.4}$$

- creation of residual values at each iteration in cycles:

$$\nabla z_{ril} = \Phi_r(\Delta_{ril}, j = \overline{1, n}); \tag{1.3.5}$$

$$z_{ril} = z_{r(i-1)l} + \nabla z_{ril}; \tag{1.3.6}$$

- conditions for completion of iterative processes in the iteration cycle:

$$\left. \begin{aligned} 1. \text{sign}(z_{rR_{\text{int},(1,2)}^*l}) &= -\text{sign}(z_{r(R_{\text{int},(1,2)}^*-1)l}) \\ \text{or} \\ z_{rR_{\text{int},(1,2)}^*l} &= 0; \\ l &= 1, 2, \dots, P_{\text{int},(1,2)}, r = \overline{1, n}. \end{aligned} \right\} \tag{1.3.7}$$

$$\left. \begin{aligned} 2. \text{sign}(z_{rR_{\text{int},(1,2)}^*l} - \text{sign}(z_{rR_{\text{int},(1,2)}^*l})) &= \\ -\text{sign}(z_{rR_{\text{int},(1,2)}^*l}) & \\ \text{or} \\ \text{sign}(z_{rR_{\text{int},(1,2)}^*l} - \text{sign}(z_{rR_{\text{int},(1,2)}^*l})) &= 0; \\ l &= 1, 2, \dots, P_{\text{int},(1,2)}, r = \overline{1, n}. \end{aligned} \right\} \tag{1.3.8}$$

In the algorithm (1.3)  $P_{\text{int},(1,2)}$  is the number of iteration cycles performed at a constant modulus of quantum;  $R_{\text{int},(1,2)}$  – a constant value, that reflects the change in the quantum of transformation and the redefinition of all variables of the linear system during the transition from cycle to cycle. The values of the constant values of  $P_{\text{int},(1,2)}$  и  $R_{\text{int},(1,2)}$

are set in accordance with those obtained in papers [14]-[16] relations:

$$P_{\text{int},(1,2)} = \left[ \frac{\ln \left| \frac{z_{0l}}{c_p^*} \right|_{\text{max}}}{\ln R_{\text{int},(1,2)}} \right], \quad (1.4)$$

where  $c_p^*$  is the weight of the minimum transformation quantum on the last cycle ( $c_p^* > 0$ ),  $R_{\text{int},1} = 2$  и  $R_{\text{int},2} = 4$ .

The number of iterations of a real computational process in a cycle  $R_{\text{int},(1,2)}^*$ , in accordance with the conclusions obtained in papers [14]-[16], may be larger or smaller relative value  $R_{\text{int},(1,2)}$ .

In the relations (1.3.1) and (1.3.2), values  $z_{r0l}$  and  $Y_{r0l}$  are calculated in the current cycle by shifting them by 1 bits at  $R_{\text{int},1} = 2$  or by 2 bits at  $R_{\text{int},2} = 4$ . This procedure reflects the introduction at the beginning of each cycle  $l$  before the fulfillment of the relations (1.3.3) - (1.3.8) of the new value of the variable quantum.

At each iteration, the quantum signs are formed as  $\pm 1$  values and are used later in next steps of the algorithm to calculate the unknown variable  $Y_{ril}$  (1.3.4) by adding or subtracting a unit from this current value.

This feature of variable normalization represents the possibility of organizing a computational process on the basis of an integer representation of data. Upon completion of the algorithm, it is possible to form the variables values to the original real representation using the weight of the minimum transformation quantum  $c_p^*$ :

$$Y_{rR_{\text{int},(1,2)}^* P_{\text{int},(1,2)}}^* = Y_{rR_{\text{int},(1,2)} P_{\text{int},(1,2)}} \cdot c_p^*, \quad (1.5)$$

where the combination  $R_{\text{int},(1,2)}^* P_{\text{int},(1,2)}$  in the index denotes the formation of the most recent value of the unknown, that is, the final result.

Completion of iterative processes in the cycles in the algorithm (1.3) is carried out on the basis of the relations (1.3.7) or (1.3.8), when in the cycle for all equations of a linear system

simultaneously or distributed in time, at least one of them is fulfilled.

### 3. ARCHITECTURE OF THE SPECIAL-PURPOSE CALCULATING UNIT FUNCTIONING USING THE FIRST ORDER DELTA-TRANSFORMATIONS AND VARIABLE QUANTUM

Figure 1 shows the block diagram of the special-purpose calculating unit functioning for the parallel iterative solution of linear systems based on the algorithm (1.3) using the first order delta-transformations and variable quantum.

Blocks 1, 4 are  $r$  registers ( $r = \overline{1, n}$ ), which contain residual  $z_{ril}$  and unknown  $Y_{ril}$  values,  $r = \overline{1, n}$  with initial values  $z_{r01} = -D_r$  and  $Y_{r01} = 0$ , respectively.

Blocks 2, 3 are registers that contain the values of the iterative cycles number performed at a constant modulus of quantum, determined before the algorithm (1.3) starts and assumes one of the two values  $R_{\text{int},(1,2)}$ , respectively.

Blocks 5, 7 are  $r$  shift registers ( $r = \overline{1, n}$ ), realizing the shift of the values  $z_{r0l}$  (1.3.1) and  $Y_{r0l}$  (1.3.2) into 1 bit at  $R_{\text{int},1} = 2$  or 2 bits at  $R_{\text{int},2} = 4$  at the beginning of each cycle  $l$ . The obtained values  $z_{r0l}$  and  $Y_{r0l}$  after the shift come to the registers of blocks 1 and 4, respectively.

In block 6, the cycles of the algorithm (1.3) are executed, the conditions for the termination of the algorithm (1.3) as a whole are verified in block 9 and the fulfillment of this condition shows the result of solving the linear system from block 4.

Blocks 8, 15 determine, according to the relations (1.3.3), the quantum signs of the first differences of the previous iteration  $z_{r(i-1)l}$  and the current iteration  $z_{ril}$  for all equations of the system.

In block 10, by the relations (1.3.4), the values of the unknowns in the group of adders ( $r = \overline{1, n}$ ) are calculated, with the input quantum signs of the first differences of variables  $\Delta_{ril}$  and the unknown values  $Y_{ril}$ . Further, the results obtained go to block 4.

In the diagram of Figure 1, the current value of the residual  $\nabla z_{ril}$  is calculated on the basis of the tabular method in the form (1.3.5), where

$$\Phi_r(\Delta_{jil}, j = \overline{1, n}) = \Delta_{ril} - \sum_{\substack{j=1 \\ (j \neq r)}}^n a_{rj} \Delta_{ji}$$

and storage devices ( $r = \overline{1, n}$ ) of the block 11 for storing the tables.

The table is organized as a sums collection of multiplication of linear system coefficients by transformation quantum for each equation. Choosing values of the pre-formed multiplication sums is performed based on the set of current values  $\Delta_{jil}, j = \overline{1, n}$  coming from block 8. Thus, it is possible to exclude the multiplication operation due to the given organization of calculations and to obtain the result for 1 unit of time.

For a large dimension of the matrix  $A$ , it is expedient to partition the sum  $\Phi_r(\Delta_{jil}, j = \overline{1, n})$  into  $m$  blocks:

$$\Phi_r(\Delta_{jil}, j = \overline{1, n}) = \sum_{g=1}^m \Phi_r^*(\Delta_{jil}, j = \overline{1, n})$$

and store the table values for each block.

Block 12 is  $r$  adders ( $r = \overline{1, n}$ ), where the input of each receives the residuals  $z_{ri}$  and values obtained in block 11 corresponding to the system equations. This block ensures the fulfillment of relation (1.3.6).

Blocks 13 and 16 are designed to verify the conditions (1.3.7), (1.3.8) that fix the moments of iterative processes completion in the  $l$ -th iteration cycle for each equation of linear system. In the block 17, the generation of these moments for all equations of the system is verified either by (1.3.7) or (1.3.8). If one of the conditions is successfully met, the counter is incremented in the block 6, and the algorithm is organized on a new iterative cycle.

In block 14, the number of iterations is counted within one iteration cycle  $l$ . This counter  $i$  is reset to zero for each next cycle  $l$ .

Blocks 12, 18 are a group of adders ( $r = \overline{1, n}$ ), in which values of the current residuals  $z_{ril}$  (1.3.6) and additional ones  $z_{add\ ril}$  are calculated, where

$$z_{add\ ril} = z_{ril} - \text{sign}(z_{ril}).$$

Performing algorithmic sequence as part of a single iteration of the algorithm (1.3) and according to that shown in Figure 1 a block diagram is carried out for about 3 units of time: at the first clock it is performed the actions of blocks 5,7; at the second clock - blocks 10,12; at the third clock - block 18.

#### 4. EXPERIMENTAL RESEARCH OF THE ALGORITHM OF A LINEAR SYSTEM SOLUTION ADAPTED UNDER REALIZATION IN A SPECIAL-PURPOSE CALCULATING UNIT

The algorithm for the parallel solution of linear system with constant free terms using the first order delta-transformations and variable quantum oriented for special-purpose calculating unit has been tested on the solution of various linear systems, characterized by different convergence in the performance of simple iteration method. Following are the results of individual experiments based on linear systems given below on (norm of the matrix coefficient  $A$  of examples (26) and (27) greater than one):

$$\begin{cases} y_1 + 0.09y_2 + 0.13y_3 = -0.97; \\ 0.12y_1 + y_2 + 0.11y_3 = -1.13; \\ 0.16y_1 + 0.07y_2 + y_3 = 1.04. \end{cases} \quad (24)$$

$$\begin{cases} y_1 + 0.6y_2 + 0.08y_3 = -0.356; \\ 0.12y_1 + y_2 + 0.7y_3 = -0.604; \\ 0.11y_1 + 0.4y_2 + y_3 = 0.353. \end{cases} \quad (25)$$

$$\begin{cases} y_1 + y_2 + 0.2y_3 = 1.37; \\ -0.8y_1 + y_2 + 0.2y_3 = 0.98; \\ -0.4y_1 + 0.7y_2 + y_3 = 1.13. \end{cases} \quad (26)$$

$$\begin{cases} y_1 + 0.9y_2 + 0.4y_3 = 0.85; \\ -y_1 + y_2 - 0.6y_3 = 0.69; \\ -1.3y_1 - 0.3y_2 + y_3 = 1.25. \end{cases} \quad (27)$$

The obtained results are presented in Table

1.

The comparative analysis between methods of linear systems solutions on the condition of ensuring identical accuracy ( $\sim 2^{-14}$ ) based on the first order delta-transformations and constant quantum  $c = 2^{-14}$ , by the simple iteration method and by the method which is also considered in this paper based on the first order delta-transformations and variable quantum at  $R_{int,1} = 2$ ,  $R_{int} = 4$ ,  $c_p^* = 2^{-14}$  was carried out during the research.

Table 1: The results of the experiments

The method of organizing the iterative process of the linear systems solution		The number of iterations			
		(24)	(25)	(26)	(27)
on the basis of the first order delta-transformations and constant quantum		210 64	196 60	176 13	191 61
on the basis of the first order delta-transformations and variable quantum	$R_{int,1} = 2$	15	19	22	51
	$R_{int,2} = 4$	18	28	31	43
simple iteration method		4	8	36	59

The analysis of the data in the Table 1 shows that the developed algorithm of iterative process of the linear systems solution with the variable quantum, based on the optimized assessments  $R_{int,1} = 2$ ,  $R_{int} = 4$ , and providing the real time optimization of the iterative processes differs significantly (by the hundreds – thousands times) with the reduction of the number of iterations in relation to the method of the linear systems solution based on the first order delta-transformations and constant quantum, as well as substantial proximity on the number of iterations to the simple iteration method and in some cases represents the advantage over the simple iteration method. According to the Table 1, the ratio of the number of iteration method for the simple iteration

method to the method with the variable quantum is as follows  $\sim 1.7-0.4$ .

The data presented in Table 1 shows that iterative processes while ensuring the convergence of linear systems may be successfully implemented even in the norm of the matrix of coefficients greater than one.

### 5. RESEARCH OF EFFICIENCY OF THE SPECIAL-PURPOSE CALCULATING UNIT FUNCTIONING BASED ON THE DEVELOPED ALGORITHM

Complex efficiency of special-purpose calculating unit functioning based on the algorithm (1.3) using delta the first order delta-transformations and variable quantum in comparison with using the simple iteration method can be considered as an interrelated set of comparative estimates of time and hardware resources. Taking into account, as shown in [14]-[16], that the number of iterations using the first order delta-transformations and variable quantum may be greater or less than using the simple iteration method, in the study this amount is assumed to be the same for the methods studied.

The implementation of the block diagram of the unit, shown in Figure 1, can be considered with using FPGA. Resource characteristics of the implementation of the basic arithmetic operations with their hardware performance are significantly unequal. Especially a huge hardware resources, expressed in logical cells, are required for multipliers. The multiplication operation will be considered as the execution of multiplication using single-clock hardware circuits of parallel implementation, as well as with the expansion of the factors bit grid - efficient parallel-serial implementation by performing multiplication at several units of time using algorithm "shift with the accumulation".

The simplest to realize the structure of simple matrix summation, which forms a parallel multiplier as an array of single-bit adder connected by local interconnections [22]. This scheme is the most effective at small bit operands (4 or less), where a parallel multiplier  $4 \times 4$  requires 12 adders for its implementation. With a further increase the bit capacity, the matrix of single-bit adder grows significantly and the critical propagation path of the transport signal increases, accordingly the performance will limited, and the realization of the

multiplier becomes less rational. Thus, in this work, multipliers of higher bit capacity were considered as a combination of multipliers  $4 \times 4$  [22].

The formation of relative estimates for hardware resources was based on the number of logical gates involved [23], [24]. In accordance with the algorithm (1.3) and the block diagram in Figure 1, the solution of the equations is carried out in parallel. Also, the equations solution is realized in parallel using the simple iteration method.

Let  $Q$  be an estimate characterizing the algorithm hardware resources,  $Q_{d.p.1}$  is the estimate for the algorithm (1.3) based on the first order delta-transformations and variable quantum,  $Q_{p.r.}$  is the estimate for the simple iteration method. For the comparative hardware estimate, it is entered the ratio  $\frac{Q_{p.r.}}{Q_{d.p.1}}$ .

Based on the obtained estimates, it was constructed the dependence graph of comparative estimates  $\frac{Q_{p.r.}}{Q_{d.p.1}}$  on the order of linear systems  $n$  ( $n \geq 3$ ), when operating with 32-bit data is shown in Figure 2.

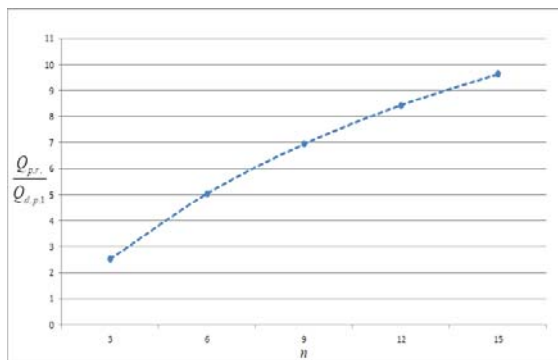


Figure 2: Dependence graph of comparative estimates  $\frac{Q_{p.r.}}{Q_{d.p.1}}$  on the order of linear systems  $n$ .

The analysis of the obtained estimates showed that using the algorithm based on the first order delta-transformations and variable quantum in a solution of linear systems of the order  $n = 3$  has an advantage in terms of hardware resources of  $\sim 2,7$  times in comparison with the simple iteration

method, when operating with 32-bit data. At increasing order of the system  $n$ , the efficiency increases.

The estimate of time resources amount was performed by units of time. Time resources were calculated in the framework of a single pass through the cycle of the algorithm, realizing algorithmic sequence of actions for all equations of the system.

Let  $T$  be an estimate characterizing the algorithm time resources,  $T_{d.p.1}$  is the estimate for the algorithm (1.3) based on the first order delta-transformations and variable quantum,  $T_{p.r.}$  is the estimate for the simple iteration method. For the comparative time estimate, it is entered the ratio  $\frac{T_{p.r.}}{T_{d.p.1}}$ .

Based on the obtained estimates, it was constructed the dependence graph of comparative estimates  $\frac{T_{p.r.}}{T_{d.p.1}}$  on the order of linear systems  $n$  ( $n \geq 3$ ), when operating with 32-bit data is shown in Figure 3.

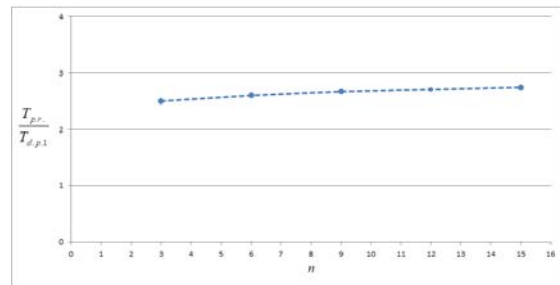


Figure 3: Dependence graph of comparative estimates  $\frac{T_{p.r.}}{T_{d.p.1}}$  on the order of linear systems  $n$ .

The analysis of the obtained estimates showed that using the algorithm based on the first order delta-transformations and variable quantum in a solution of linear systems of the order  $n = 3$  has an advantage in terms of time resources of  $\sim 2,5$  times in comparison with the simple iteration method, when operating with 32-bit data. At increasing order of the system  $n$ , the efficiency increases.

Complex efficiency of special-purpose calculating unit functioning based on the algorithm (1.3) using delta the first order delta-transformations and variable quantum

The comparative complex estimate of the algorithm implementation efficiency based on using delta the first order delta-transformations and variable quantum was formed as the multiplication of the relative estimates, obtained above, for the time and hardware resources of the system:

$$E = \frac{Q_{p.r.}}{Q_{d.p.1}} \cdot \frac{T_{p.r.}}{T_{d.p.1}}. \quad (1.6)$$

In accordance with (1.6), the comparative complex efficiency estimate were obtained. Based on these results, it was constructed the dependence graph of the comparative complex efficiency estimate  $E$  on the order of linear systems  $n$  and is shown in Figure 4.

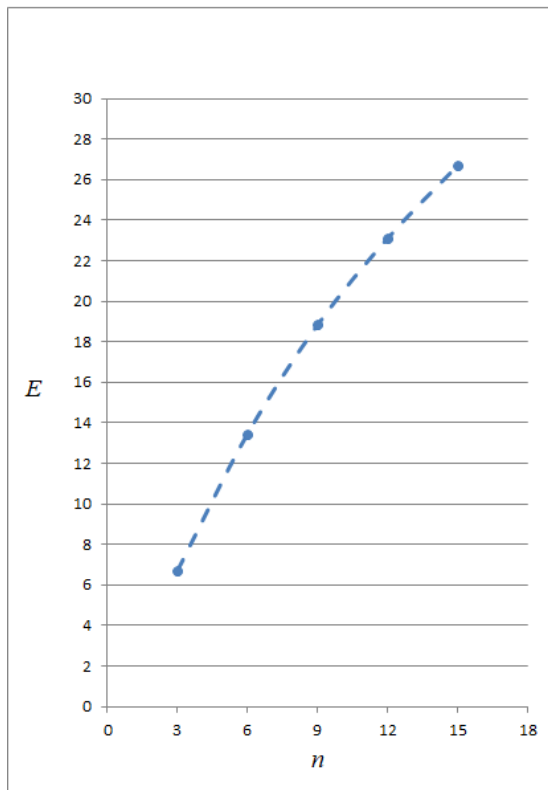


Figure 4: Dependence graph of the comparative complex efficiency estimate  $E$  of special-purpose calculating unit functioning on the order of linear systems  $n$ .

In accordance with this estimate  $E$ , the algorithm based on the first order delta-transformations and variable quantum, when solving linear systems of the  $n = 3$  order, has an advantage ~6.7 times compared to using the simple iteration method, when operating with 32-bit data. The estimate with an increase in the order of the system  $n$  sharply increases.

## 5. CONCLUSION

In paper it is considered principles of special-purpose calculating unit functioning for the iteration solution of linear systems using the first order delta-transformations and variable quantum. A special feature of special-purpose calculating unit functioning based on this algorithm is the implementation of the introduction at the beginning of each cycle  $l$  of a new variable quantum value that is reflected in the current cycle when the residual values and the unknown variable are formed by shifting them to the left by 1 or 2 bits. Formation of unknown variables in the unit is carried out by adding or subtracting the signs of quantum of the first and second variables differences, taking at each iteration the values  $\pm 1$ . This feature of variable normalization represents the possibility of organizing a computational process on the basis of an integer data representation. At the final step of the algorithm operation in the unit, it is possible to bring the values of the variables to the original real form, taking into account the weight of the minimum transformation quantum.

In addition, the feature of special-purpose calculating unit functioning is using in the algorithm for forming the moment of iterative processes completion in cycles the condition that requires when in the cycle for all equations of a linear system simultaneously or distributed in time the residuals sign will be changed.

The architecture of the special-purpose calculating unit impact on final algorithmic efficiency: as it is shown in this work the possibilities of the organization of simultaneous shifts of coefficients with preservation of shift of the previous cycle or without preservation that can be connected with need of essential expenses of the hardware or time resources. And there is no doubt that the level of the parallelization processes has an impact on the performance of the special-purpose calculating unit. The ultimate effectiveness is estimated by the aggregate of the assessments of the hardware resources and the speed.



In the paper it is shown the advantages of using the developed features of the special-purpose calculating unit functioning based on the linear system solution using the first order delta-transformations and variable quantum. So with the orientation to FPGA, comparative estimates are obtained for the hardware resources and the speed of the developed algorithm for special-purpose calculating unit functioning. For the developed algorithm of the unit functioning, it is shown that it is possible to reduce the execution of one iteration and the iterative process as a whole in  $\sim 2,5$  times and the amount of hardware resources in  $\sim 2,7$  times in comparison with the special-purpose calculating unit functioning based on the simple iteration method, and these estimates increase with increasing order of the system  $n$ .

Thus, using the adapted algorithm for building special-purpose calculating unit, the hardware resources are significantly reduced due to the ability to exclude the multiplication operator of the multibit code and increasing the performance of the simple iteration method at the level of the iterative processes. These circumstances create prerequisites associated, in particular, with the expansion of the resource capabilities of FPGAs for the simultaneous realization in real time of complex tasks, as separate components of which are used linear systems.

The results obtained in this work are planned to be used in on-board control and navigation systems by modern aircraft. As a separate component of the navigation task solved within the framework of the creation of on-board specialized computing devices, the task of determining the aircraft coordinates is presented, represented as systems of linear algebraic equations. Based on the results of preliminary studies, using the principles of special-purpose calculating unit functioning, presented in this paper, in on-board specialized devices for the local navigation task makes it possible to determine the aircraft coordinates at each time step of the steady-state process in one iteration and operate with practically significant time steps of the system at a sufficiently large distance of the beginning of the steady process from the origin.

#### REFERENCES:

- [1] P.P. Kravchenko, L.V. Pirkaya and N.Sh. Khusainov, "Delta-transformations and problem-oriented computations: monograph". *Taganrog: SFedU Press*, 2016.
- [2] Sh. Khusainov, P.P. Kravchenko, V.N. Lutai, S.A. Tarasov and V.V. Scherbinin, "Radionavigation systems of current-technology and future-technology vehicles. P. 1. Fixing methods and autonomous integrity monitoring: Monograph", *Taganrog: SFedU Press*, 2015.
- [3] O.O. Barabanov and L.P. Barabanova, "Mathematical problems of r-r navigation", *Moscow: Phismatlit*, 2007.
- [4] O.N. Skrypnik, "Aircraft radionavigation systems: Training manual", *Moscow: INFRA-M*, 2014.
- [5] C. Vuik, "Iterative solution methods", *The Netherlands: Delft Institute of Applied Mathematics*, 2012.
- [6] A. Greenbaum, "Iterative Methods for Solving Linear Systems", *Philadelphia, PA: SIAM*, 1997.
- [7] D.K. Faddeev and V.N. Faddeeva, "Computational methods of linear algebra, 4th ed., reprint", *Saint-Petersburg: Lan'*, 2009.
- [8] N. Bakhvalov, *et al.*, "Numerical Methods", *Laboratory of knowledge. Moscow: BINOM*, 2006.
- [9] S. Tretyakov, "The algorithms of the specialized processors for solving systems of equations", *Cybernetics, Vol. 5*, 1978, pp. 34-36.
- [10] P.P. Kravchenko, "The optimised second order delta-transformations. Theory and application. Monograph", *Moscow: Radio engineering*, 2010.
- [11] P. Kravchenko, "Incremental methods for solving the systems of the linear algebraic equations", *Multiprocessor computing structures, Vol. 5(XIV)*, 1983, pp. 30-32.
- [12] B. Malinowskij, "Algorithms for solving the systems of the linear algebraic equations, structural-oriented implementation", *Control systems and machines, Vol. 5*, 1977, pp. 79-84.
- [13] O. Gomofov and Y. Ladyshchenskij, "Incremental algorithms for solving the systems of the linear algebraic equations, and architecture of multiprocessors on programmable logic", *Scientific works of DonNTU, Informatics, cybernetics and computer science, Vol. 12(165)*, 2010, pp. 34-40.
- [14] P.P. Kravchenko and L.V. Pirkaya, "The iterative method of the system of the linear algebraic equations solution excluding the

- multidigit multiplication operation”, *Izvestia SFedU. Engineerin sciences*, Vol. 7 (156), 2014, pp. 214-224.
- [15] L.V. Pirkaya, “Iterative Algorithm for Solving of Linear Algebraic Equations Systems without Multi-bit Multiplication Operation”, *Engineering and Telecommunication (EnT), 2014 International Conference on*, Moscow, Russia, 2014, pp. 87-91.
- [16] P.P. Kravchenko and L.V. Pirkaya, “The method of organizing the iterative process of the system of the linear algebraic equations solution excluding the multidigit multiplication operation”, *Biosciences Biotechnology Research Asia*, Vol. 11(3), 2014, pp. 1831-1839.
- [17] L.V. Pirkaya, “On the features of the functioning of a specialized computing device for the iteration solution of linear systems based on delta- transformations algorithms”, *Proceedings of the XXIV Scientific Conference “Modern Information Technologies: Trends and Development Prospects”*; Southern Federal University, Russia, May 25, 2017, pp.159-160.
- [18] L.V. Pirkaya, “On the optimized performance of a specialized computing device based on delta transformations of the first and second orders”, *Proceedings of the Conference “Information Systems and Technologies: Fundamental and Applied Research”*, Southern Federal University, Russia, October 23-27, 2017.
- [19] H. Yang and S.G. Ziavras, “FPGA-based vector processor for algebraic equation solvers”, *SOC Conference, Sept. 19-23, Herndon, VA, IEEE*, 2005, pp. 115-116.
- [20] W. Zhang, V. Betz, and J. Rose, “Portable and Scalable FPGA-Based Acceleration of a Direct Linear System Solver”, *ICECE Technology, Taipei, IEEE*, December 8-10, 2008.
- [21] W. Zhang, *et al.*, “Portable and Scalable FPGA-Based Acceleration of a Direct Linear System Solver”, *ACM Transactions on Reconfigurable Technology and Systems (TRETs)*, vol. 5 (1), 6, 2012.
- [22] V. Steshenko, “Lesson 6. Implementation of computing devices on FPGA”, *Components and technologies*, Vol. 6, 2000, pp. 88-91
- [23] N.V. Maksimov, T.L. Partyka, I.I. Popov “Architecture of computers and computer systems”, *Moscow: FORUM*, 2010.
- [24] E. Tanenbaum, T. Austin “Computer architecture”, *St. Petersburg: Peter*, 2013.

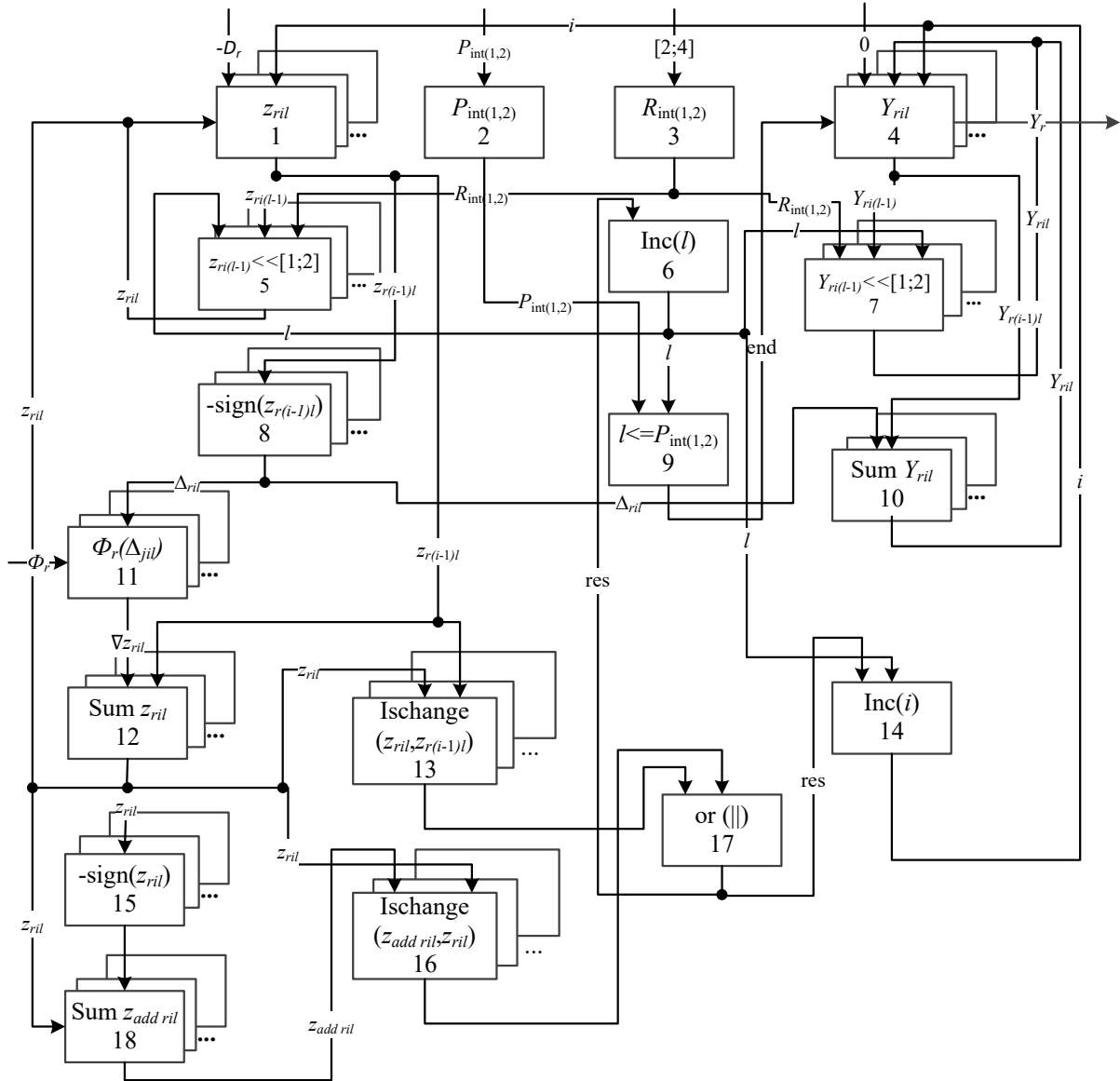


Figure 1: The block diagram of the special-purpose calculating unit functioning for the parallel iterative solution of linear systems using the first order delta-transformations and variable quantum