<u>31<sup>st</sup> December 2011. Vol. 34 No.2</u> © 2005 - 2011 JATIT & LLS. All rights reserved.

ISSN: 1992-8645

<u>www.jatit.org</u>



# COMPARISON OF FULL BRIDGE AND TWO INDUCTOR BOOST CONVERTER SYSTEMS

# G. KISHOR<sup>1</sup>, D.SUBBARAYUDU<sup>2</sup>, AND S.SIVANAGARAJU<sup>3</sup>

<sup>1</sup> Associate Professor, G.Pulla Reddy Engineering College, Kurnool, India.
 <sup>2</sup> Senior Professor, BITS, Kurnool, India.
 <sup>3</sup> Associate Professor, JNTU, Kakinada, India.
 E-mail: <u>gudipatikishor@gmail.com</u>, <u>sirigiri70@yahoo.co.in</u>

#### ABSTRACT

The DC-DC converter topologies have received increasing attention in recent years for high power and high performance applications. The advantages of DC-DC boost converters include increased efficiency, reduced size, reduced electromagnetic emission, faster transient response, and improved reliability. The front end inductors in a boost converter are magnetically coupled to improve electrical performance and reduce size and weight. It features several merits such as multiple-output capability with one shared choke, single-stage energy processing and good input current shaping ability. This paper compares conventional boost converter and two inductor boost converter designed for high voltage and high power applications. Results of conventional boost converter are compared with two inductor boost converter.

Keywords: Comparison, Power Factor, Two Inductor Boost Converter, Bridge,

## **1. INTRODUCTION**

DC/DC Converters are mostly used for interconnections of two DC networks with different voltage levels. There are many different topologies, which varies in complexity of circuits, stress on used components and quality of input and output power.

The boost converter topology has been extensively used in various ac/dc and dc/dc applications. In fact, the front end of today's ac/dc power supplies with power factor correction is almost exclusively implemented with boost topology.

In the high-power bi-directional dc/dc converter, the isolated full-bridge boost converter has been a good choice due to its effectiveness to limit the overshoot of bridge switch's turn off voltage and to enable the energy stored in the transformer leakage inductance to be used for zero voltage switching [2]. Effectiveness of this circuit topology has been demonstrated in the applications of the alternative energy systems, or the hybrid vehicle systems [3].

Graphically model of the boost converter is shown in fig.1. The full details of the boost

converter topology have been already discussed in [4, 5, 6].

Generally, a single-inductor, single-switch boost converter topology and its variations exhibit a satisfactory performance in the majority of applications where the output voltage is greater than the input voltage. The performance of the boost converter can be improved by implementing a boost converter with multiple switches and/or multiple boost inductors.

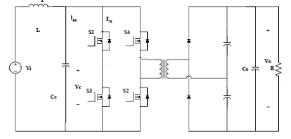


Fig.1.Conventional boost converter

The two inductor boost converter exhibits benefits in high power applications [7]-[14]: high input current is split between two inductors, thus reducing I<sup>2</sup>R power loss in both copper windings and primary switches. Furthermore, by applying

# Journal of Theoretical and Applied Information Technology

<u>31<sup>st</sup> December 2011. Vol. 34 No.2</u> © 2005 - 2011 JATIT & LLS. All rights reserved.

```
ISSN: 1992-8645
```

<u>www.jatit.org</u>



E-ISSN: 1817-3195

an interleaving control strategy, the input current ripple can be reduced [9]. Implementation of the topology can be in either non isolated [15] or isolated format. The isolated boost topology, which is shown in Fig. 2 [16], is attractive in applications such as power factor correction (PFC) with isolation and battery or fuel cell powered devices to generate high output voltage from low input voltage[15]-[18].

The main obstacle of the circuit in Fig.2 is its limited power regulation range. Inductor  $L_1$  must support input voltage when-ever  $Q_1$  turns on. Likewise, this is true for  $L_2$  and  $Q_2$ . Since the minimum duty ratio of each switch is 0.5, the magnetizing currents of the two inductors cannot be limited. This leads to a minimum output power level. If the load demands less power than this minimum level, the output voltage increases abnormally because excessive energy has been stored in the inductors.

A recent solution to this limitation on minimum power is given in Fig.3 [17], [18]. An auxiliary transformer  $T_2$  is inserted in series with inductor  $L_1$ and  $L_2$ . Transformer  $T_2$  magnetically couples two input current paths.

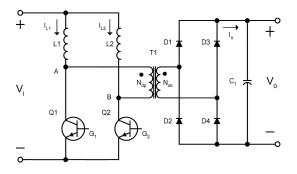


Fig.2.Conventional two inductor boost converter

The currents in the two inductors are then forced to be identical. Theoretically, the input current only increases when both  $Q_1$  and  $Q_2$  turn on. If the overlapping between two driving signals is small, the inductor currents become discontinuous. This improvement makes the two-inductor boost circuit attractive in application.

However, a disadvantage of the app-roach is that the circuit requires four magnetic components on the primary side, thus, requiring additional board space.

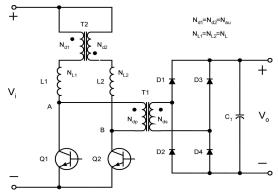


Fig.3. Two-inductor boost converter with auxiliary transformer

Advantages of the topology include the properties that it does

- 1) Implements the isolated two-inductor boost converter with one magnetic assembly, thereby reducing the board space.
- Maintains wide power regulation range: that is, under the condition that the output voltage is regulated, the input power is limited when the overlapping of driving signals is small.
- 3) Has a reduced number of windings (two windings) on the primary side of the circuit compared to the topology in Fig. 3 (five windings). The copper loss can be reduced because of fewer windings and soldering connections.
- 4) Implements the start-up and protection windings within the same magnetic assembly without adding components to the primary circuit.

New integrated magnetic DC to DC converter is given by Bloom [13]. Modern switch mode DC to DC converters are given by Severns [14]. Core selection and design aspects of magnetic forward converter is given in [15]. Modelling and analysis of magnetic components is given by Cheng [16]. 1- $\varphi$  UPF AC to DC boost converter is given by Bhim Singh [17]. Timer controller with constant frequency is given by Marcos [18].

In the literature [1] to [24] comparison of boost converter with two inductor boost converter is not present. In this work the parameters like input voltage, power and efficiency are presented.

## 2. ANALYSIS

The voltage across the inductor during on period is  $V_d$ . During this period the current raises

<u>31<sup>st</sup> December 2011. Vol. 34 No.2</u> © 2005 - 2011 JATIT & LLS. All rights reserved.

ISSN: 1992-8645	www.jatit.org	E-ISSN: 1817-3195

linearly from a minimum level to maximum level. Therefore the voltage across the inductor is

$$V_{\rm L} = V_{\rm d} \tag{1}$$

And also

$$V_L = L (I_2 - I_1) / T_{on} = L(\Delta I) / T_{on}$$
 (2)

From (1) and (2)

$$T_{on} = L(\Delta I) / V_d$$
(3)

The voltage across the inductor during off period is  $(V_o-V_d)$  and the current drops linearly. Therefore the voltage across the inductor is

$$V_{\rm L} = (V_{\rm o} - V_{\rm d}) \tag{4}$$
 Also

$$V_{L} = L (I_{2}-I_{1}) / T_{off} = L(\Delta I) / T_{off}$$
 (5)

From (4) and (5)

$$T_{\rm off} = L(\Delta I) / (V_{\rm o} - V_{\rm d})$$
(6)

From (3)

$$L(\Delta I) = T_{on} * V_d \tag{7}$$

From (6)

$$L(\Delta I) = T_{off} * (V_o - V_d)$$
(8)

From (7) and (8)

$$\begin{array}{c} T_{on} * V_d = T_{off} * (V_o\text{-}V_d) \\ Or & V_o = (T_{on}\text{+}T_{off}) * V_d \ / \ T_{off} \end{array}$$

 $Or \qquad \quad V_o = T \, * \, V_d \, / \, T_{off}$ 

$$Or V_o = V_d / (1-\alpha) (9)$$

Where  $\alpha$  is the delay angle. As firing angle varies, the output voltage increases from V<sub>d</sub> to infinity. Hence the output voltage is boosted.

The selection of inductor and the capacitor in the Boost topology plays a major role in the output response. The inductance value is obtained using

$$L = \frac{V_0 D}{f \Delta I} \tag{10}$$

Where *f* is the switching frequency, *D* is the duty ratio,  $V_0$  is the source voltage and  $\Delta I$  is the peak to peak ripple current.

The Capacitance value is obtained using

$$C = \frac{D}{2 fR} \tag{11}$$

#### **3. SIMULATION RESULTS**

Conventional two inductor boost converter is shown in Fig 4a. DC input voltage and current are shown in Fig's 4b and 4c respectively. DC input power is shown in Fig 4d. Driving pulse and voltage across the switches S1 and S2 are shown in Fig's 4e and 4f respectively. DC output voltage is shown in Fig 4g. DC output current is shown in Fig 4h. The circuit of two inductor boost converter is shown in Fig 5a. DC input voltage and current waveforms are shown in Fig's 5b and 5c respectively. Switching pulses and output voltage of  $S_1$  and  $S_2$  are shown in Fig's 5d and 5e respectively. DC output voltage, current and power are shown in Fig's 5f, 5g and 5h respectively. Table of comparison is given in Table 1.Comparison of output power is shown in Fig 5i. Comparison of efficiency is shown in Fig 5j. The efficiency increases by 2%.

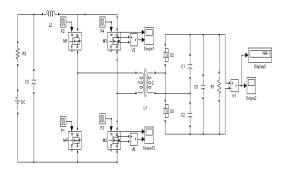
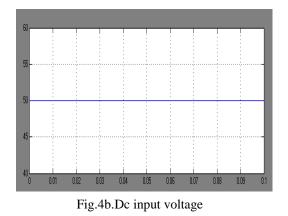


Fig.4a. Conventional two inductor boost converter



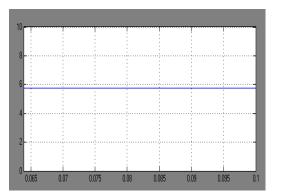
# Journal of Theoretical and Applied Information Technology <u>31<sup>st</sup> December 2011. Vol. 34 No.2</u> © 2005 - 2011 JATIT & LLS. All rights reserved.



E-ISSN: 1817-3195

www.jatit.org

ISSN: 1992-8645





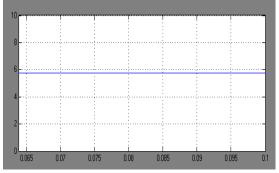


Fig.4d.Dc input power

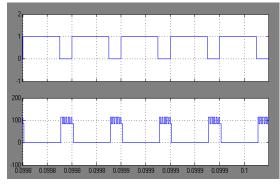
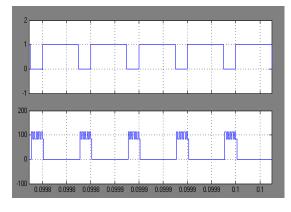
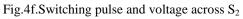


Fig.4e.Switching pulse and voltage across S1





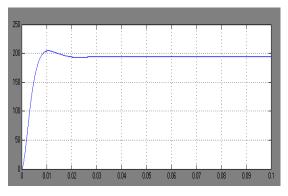


Fig.4g.Dc output voltage

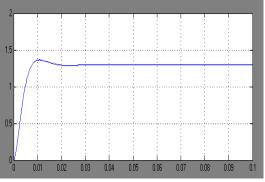


Fig.4h.Dc output current

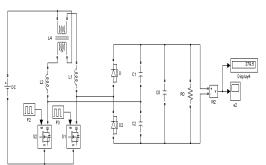


Fig.5a.Circuit diagram of Two Inductor Boost Converter

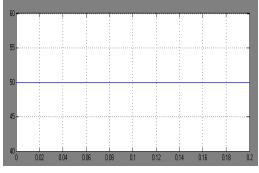


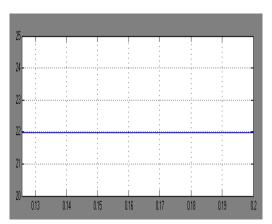
Fig.5b.Dc input voltage

# Journal of Theoretical and Applied Information Technology <u>31<sup>st</sup> December 2011. Vol. 34 No.2</u> © 2005 - 2011 JATIT & LLS. All rights reserved.

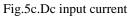
www.jatit.org

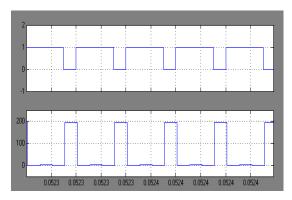


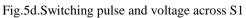
E-ISSN: 1817-3195

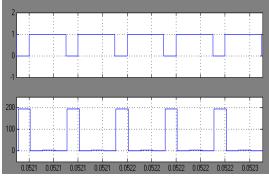


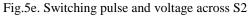
ISSN: 1992-8645

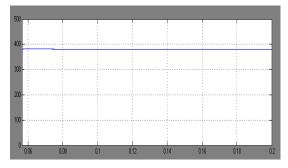


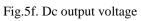












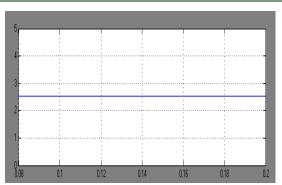


Fig.5g. Dc output current

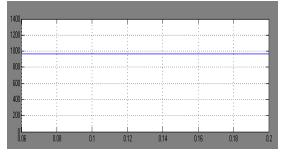
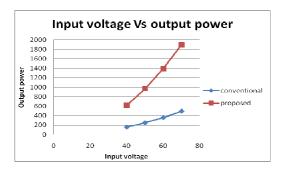
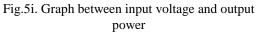


Fig.5h. Dc output power

Table 1: Comparison

<b>V</b> :	Input power		Output power		Efficiency	
Vi n	Con venti onal	Pro pos ed	Con venti onal	Prop osed	Conve ntiona 1	Prop osed
40	185	702	159	617	85.95	87.8
						9
50	290	110	251	969	86.55	88.0
		0				9
60	416	156	361	139	86.78	88.9
		8		4		0
70	570	213	495	190	86.84	88.9
		5		0		9





<u>31<sup>st</sup> December 2011. Vol. 34 No.2</u> © 2005 - 2011 JATIT & LLS. All rights reserved.

ISSN: 1992-8645

www.jatit.org

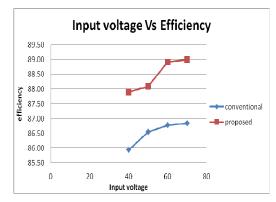


Fig.5j. Graph between input voltage and efficiency

# 4. CONCLUSION

Full bridge boost converter and Two inductor boost converter systems are simulated using simulink. Two inductor boost converter has advantages like reduced hardware, reduced transformer size and filter requirement. Two inductor boost converter is capable of giving higher output power. The efficiency increases by 2%. Therefore two inductor boost converter is better than full bridge type boost converter.

## REFERENCES

- Yakushev, V.; Meleshin, V.; Fraidlin, S. " Full-bridge isolated current fed converter with active clamp" Applied Power Electronics Conference and Exposition, 1999. APEC '99. Fourteenth Annual, Volume: 1, 1999 Page(s): 560 -566 vol.
- [2] Watson, R., Lee, F.C., "A soft-switched, full-bridge boost converter employing an active-clamp circuit," IEEE PESC, 1996, Volume: 2, Page(s): 1948 -1954.
- [3] Lizhi Zhu; Kunrong Wang; Lee, F.C.; Jih-Sheng Lai, "New start-up schemes for isolated full-bridge boost converters," IEEE APEC 2000, Page(s): 309-313 vol.1
- [4] Mohammad H. Rashid, "Power Electronics: Circuits, Devices and Applications", Prentice-Hall, Inc., Englewood Cliffs, Book, Second Edition, 1993.
- [5] B. W. Williams, "Power Electronics; Devices, Drivers, Applications and Passive Components", Book, Second Edition,

Educational Low-Priced Books Scheme; ELBS, 1992.

- [6] Mohan Ned, Undeland Tore M. and Robbins William P.,"Power Electronics, Converters Applications and Design", John Wiley & Sons, Inc., Book, 1995.
- [7] B.A.Miwa, D.M.Otten, and M.F.Schlecht, High efficiency power factor correction using interleaving techniques," in Prloc.IEEE APEC'92 Conf, 1992, pp.557–568.
- [8] J.W.Kolar, G.R.Kamath, N.Mohan, and F.C.Zach, "Self adjusting input current ripple cancellation of coupled parallel connected hysteresis controlled boost power factor correctors," in Proc.IEEEPESC'95Conf. 1995, pp.164–173.
- [9] M.S.Elmore, "Input current ripple cancellation in synchronized, parallel connected critically continuous boost converters," in Proc.IEEEAPEC'96Conf, 1996, pp.152–158.
- [10] J.R.Pinheiro, H.A.Grundling, D.L.R.Vidor, and J.E.Baggio, "Control strategy of an interleaved boost power factor correction converter," in Proc.IEEE PESC'99 Conf., 1999, pp.137–142.
- [11] H.A.C.Braga and I.Barbi, "A3-kW unity-power-factor rectifier based on a two-cell boost converter using a new parallel-connection technique, IEEE Trans.PowerElectron" vol.14, no.1, pp.209–217, Jan.1999.
- [12] B.T.Irving, Y.Jang, and M.M.Jovanovic, "A comparative study of soft-switched CCM boost rectifiers and interleaved variable-frequency DCM boost rectifier," in Proc. IEEE APEC'00 Conf.,2000, pp.171–177.
- [13] A.V.D.Bossche, V.Valtchev, J.Ghijselen, and J.Melkebeek, "Two-phase zero-voltage switching boost converter for medium power applications," in Proc. IEEE Industry Applications Soc. Annu.Meeting, New Orleans,LA,1998,pp.1546–1553.
- [14] P.J.Wolfs, "A current-sourced dc-dc converter derived via the duality Principle from the half bridge converter," IEEE Trans.Ind.Electron., vol.40, no.1, pp.139-144, Feb.1993.

© 2005 - 2011 JATIT & LLS. All rights reserved.

ISSN: 1992-8645

www.jatit.org

- [15] M.T.Zhang, Y.Jiang, F.C. Lee, and M.M.Jovanovic, "Single-phase three-level boost power factor correction converter," in Proc.IEEE APEC'95Conf. 1995, pp.434–439.
- [16] G.Ivensky, I.Elkin, and S.Ben-Yaakov, "An isolated dc/dc converter using two zero current switched IGBT's in a symmetrical topology," in Proc.IEEE PESC'94 Conf., 1994, pp.1218–1225.
- [17] Y.Jang and M.M.Jovanovic, "New two-inductor boost converter with auxiliary transformer," in Proc.IEEEAPEC'02Conf, 2002, pp. 654–660.
- [18] Y.Jang and M.M.Jovanovic, "Two-Inductor Boost Converter," U.S. Patent6239584, May29, 2001.
- [19] E.Bloom, "New integrated-magnetic dc-dc power converter circuits & systems," in Proc.IEEE APEC'87 Conf., 1987, pp.57–66.
- [20] R.Severns and E.Bloom, Modern DC/DC Switch mode Power Converter Circuits. New York: Van Nostrand Reinhold, Dec.1985.
- [21] E.Bloom, "Core selection for & design aspects of an integrated-magnetic forward converter," in Proc.IEEE APEC'86 Conf., 1986, pp. 141–150.
- [22] D.K.Cheng, L.Wong, and Y.S.Lee, "Design, modeling, and analysis of integrated magnetics for power converters," in Proc.IEEE PESC'00 Conf., 2000, pp.320–325.
- [23] A.Pandey, Prof B.Singh, "Comparative Evaluation of Single-phase Unity Power Factor ac-dc Boost Converter Topologies", IEEE proc, November 30, 2004.pp, 102-109.
- [24] Marcos, T.Galelli, Marcio, S.Vilela, "Proposal of a timer controller with constant switching frequency and power factor correction", IEEE, 2005.pp, 102-109.

# **AUTHOR PROFILES:**



**G.Kishor** has obtained his B.E from Bangalore University in the year 2001. He has obtained his M.E from Sathyabama University in the year 2004. He has 6 years of reaching experience. Presently he

is a research scholar in JNTU, Anantapur. He is working in the area of high power density boost converters.



**Dr. D. Subba Rayudu** received B.E degree in Electrical Engineering from S.V.University, Tirupati, India in 1960, M.Sc (Engg) degree from Madras University in 1962 and Ph.D degree from Indian Institute Madras India in 1977 He is

of Technology, Madras, India in 1977. He is working as professor in the Department of Electrical and Electronics Engineering, BITS, Kurnool, India. His research interests include Power Electronic Converters.



**Dr.S.Sivanagaraju** received his Masters degree in 2000 from IIT, Kharagpur and did his Ph.D from J.N.T.University in 2004. He is currently working as associate professor in the department of Electrical Engineering.

J.N.T.U.College of Engg, Kakinada, Andhra Pradesh, India. He had received two national awards (Pandit Madan Mohan Malaviya memorial prize award and Best paper prize award) from the institution of engineers (India) for the year 2003-04. He is referee for IEE Proceedings-Generation Transmission and Distribution and International journal of Emerging Electrical Power System. He has 40 publications in National and International journals and conferences to his credit. His areas of interest are in Distribution Automation, Genetic Algorithm application to distribution systems and PowerElectronics.